

NEMA TS2 Fully-Actuated Advanced Traffic Controller Software

This specification is fully met by the following Econolite model:

EOS Software

Table of Contents

[1. Introduction 4](#_Toc28957030)

[2. Displays 4](#_Toc28957031)

[2.1 Dynamic Displays 4](#_Toc28957032)

[2.2 Programming Displays 6](#_Toc28957033)

[3. Programing 6](#_Toc28957034)

[3.1 Programming Methods 6](#_Toc28957035)

[3.2 Programming Security 7](#_Toc28957036)

[3.3 Programming Utility Functions 7](#_Toc28957037)

[4. Actuated Control Functions 8](#_Toc28957038)

[4.1 Phase Sequence 8](#_Toc28957039)

[4.2 Timing Intervals 9](#_Toc28957040)

[4.3 Overlaps 11](#_Toc28957041)

[4.4 Conditional Service 12](#_Toc28957042)

[4.5 Additional Features 13](#_Toc28957043)

[5. Coordination 15](#_Toc28957044)

[5.1 Event plans 15](#_Toc28957045)

[5.2 Cycle Length 16](#_Toc28957046)

[5.3 Synchronization 16](#_Toc28957047)

[5.4 Offset 16](#_Toc28957048)

[5.5 Split 16](#_Toc28957049)

[5.6 Permissive Periods 17](#_Toc28957050)

[5.7 Phase Re-service 17](#_Toc28957051)

[5.8 Transition Cycles 17](#_Toc28957052)

[5.9 Crossing Artery Control 18](#_Toc28957053)

[5.10 Adaptive Split Demand 18](#_Toc28957054)

[5.11 Free Mode 18](#_Toc28957055)

[5.12 Manual Control 18](#_Toc28957056)

[5.13 Interconnect Modes 19](#_Toc28957057)

[6. Preemption 19](#_Toc28957058)

[6.1 Railroad-Fire-Emergency Vehicle Preemption 19](#_Toc28957059)

[6.2 Preemption Safeguards 21](#_Toc28957060)

[6.3 Signal Control and Priority 22](#_Toc28957061)

[7. Time-Based Control (Non-Interconnected) Coordination 23](#_Toc28957062)

[7.1 Clock/Calendar Functions 23](#_Toc28957063)

[7.2 Time-Based Control 23](#_Toc28957064)

[7.3 Time-Based Coordination 24](#_Toc28957065)

[8. Cabinet 25](#_Toc28957066)

[8.1 Cabinet Functions 25](#_Toc28957067)

[9. Detectors 25](#_Toc28957068)

[9.1 Detector Functions 25](#_Toc28957069)

[9.2 Detector Cross Switching 25](#_Toc28957070)

[9.3 Detector Types 26](#_Toc28957071)

[9.4 System Detectors 26](#_Toc28957072)

[10. Logic Processor Commands 26](#_Toc28957073)

[10.1 Standard Logic Commands 26](#_Toc28957074)

[10.2 Extended Options Logic commands 28](#_Toc28957075)

[11. System Communications 29](#_Toc28957076)

[11.1 System Commands 29](#_Toc28957077)

[11.2 System Communications 30](#_Toc28957078)

[11.3 Communications Protocols 30](#_Toc28957079)

[11.4 Ethernet Communications 30](#_Toc28957080)

[11.5 External Clock 30](#_Toc28957081)

[12. Diagnostics 30](#_Toc28957082)

[12.1 General Diagnostics Features 30](#_Toc28957083)

[12.2 Detector Diagnostics 31](#_Toc28957084)

[13. Logging 31](#_Toc28957085)

[13.1 Detector Logging 31](#_Toc28957086)

[13.2 Detector Failure Logging 31](#_Toc28957087)

[13.3 Event Logging 32](#_Toc28957088)

1. Introduction

This specification sets forth the minimum requirements for software that operates a two (2) through 16 (sixteen) phase, fully-actuated, digital, solid-state traffic controller. The controller shall meet, as a minimum, all applicable sections of the NEMA Standards Publications for TS2 and NTCIP 1202 and ATC standard 6.25. Where differences occur, these specifications shall govern. Controller versions shall be available to comply with NEMA TS2 Types 1 and 2. Type 2 versions of the controller shall be capable of operating as a Type 1 controller. The software additionally supports Caltrans TEES approved 2070C traffic controllers, provided the hardware vendor has provided Econolite with the toolkit for application cross-porting.

1. Displays

## Dynamic Displays

* + 1. Dynamic displays listed below shall be provided to show the operational status of the controller. Additional displays shall be offered for programming. It shall be possible to jump from a status screen to the appropriate programming screen and return after viewing is complete. When the cursor is moved to a dynamic location on the screen, an audible tone shall be provided when the display changes. When the cursor is moved around the status screen and the help key is pressed, a context sensitive help shall be displayed. The intersection, controller, coordination, preempt, and time-of-day (TOD) status displays shall contain a title line that displays the status being viewed, the controller status, the current date, and time.

2.1.1.1 **The INTERSECTION STATUS DISPLAY** shall indicate a summary of ring, phase, coordination, preemption, TSP, communications, logic processor flags, and time-based control status. It shall be possible to place vehicle, pedestrian, preemption, and TSP calls from the keyboard while displaying status information.

2.1.1.2 The **CONTROLLER STATUS DISPLAY** shall indicate current interval, pedestrian, density, maximum, and maximum extension timing by phase and ring. The status of vehicle, pedestrian, HAWK, and overlap signal outputs shall be displayed in combination with vehicle and pedestrian calls. The display shall also show the split plan, timing plan, sequence, event plan, and day plan in effect. The display shall include the status of the first sixteen logic processor flags.

2.1.1.3 The **COORDINATOR STATUS DISPLAY** shall indicate the status of vehicle signal outputs in combination with vehicle and pedestrian calls. The display shall also show the split plan, timing plan, sequence, event plan and day plan in effect. Also displayed shall be, current event plan information, local and system cycle count, time-based control status, hold, force-off, vehicle permissive, pedestrian permissive, split count down, split extension, and offset from ring 1.

2.1.1.4 The **PREEMPTOR STATUS DISPLAY** shall indicate the status of vehicle, pedestrian overlap and overlap signal outputs shall be displayed in combination with vehicle and pedestrian calls. Priority (railroad, fire, emergency) preemptors and bus preemptors with calls, preemptor active, inhibit, and delay status. When a preemptor is active, the display shall also indicate preemptor interval, timing, duration, and dwell status. A portion of the display shall indicate the controller status during preemption including current status, interval, and timing by phase and ring and the status of vehicle and pedestrian signals for each phase.

2.1.1.5 The **SCP STATUS DISPLAY** shall indicate the status of top four priority requests in NTCIP 1211 Priority Request Server with class type, ETA, phase, and ID of each request. Overrides for phase splits, hold, and force-off shall also be available.

2.1.1.6 The **TIME BASE STATUS** display shall indicate the current time and date the source of event plan selection, the next event plan to be selected, the current schedule, day of the week, event plan number and event plan start time. This status screen shall display a total of 30 TOD events.

2.1.1.7 The **COMMUNICATION STATUS** displays shall be communications status displays for Ethernet, Port 1 (SDLC), Port 2 (terminal) Port 3, and NTCIP.

a. An Ethernet status display shall indicate the line speed, the line status, the total number of transmits and receive counts and the number of transmit and receive error counts.

b. Port 1 (SDLC) status display shall indicate the frame responses from the MMU, the terminal and facilities BIUs and the detector BIUs. (applies to NEMA controllers hardware only)

c. Ports 2 and 3 status display shall indicate the interconnect format, transmit, valid data, data error, carrier detect and the last valid command. (when ports exist on controller hardware)

d. An NTCIP status display shall indicate the total number of SNMP and STMP transmit and receive counts.

2.1.1.8 The **DETECTOR STATUS** display shall indicate activity for up to 64 detectors. The display shall show detector calls as they are processed by the controller. The display shall also show the extension and delay timers for the selected detector. In addition, the failure status of the detector shall be displayed.

2.1.1.9 The **FLASH/MALFUNCTION MANAGEMENT UNIT (MMU**) status display shall indicate flash status plus MMU channel, conflict, and monitoring function status. A separate display shall indicate the results of the controller’s comparison of its MMU programming to the programming in the controller. (Not available for cabinet configurations that use a 2010 CMU.)

2.1.1.10 The **INPUT AND OUTPUT STATUS** displays shall indicate the activity of all of the logic level inputs and outputs to the controller.

2.1.1.11 The **SPAT STATUS** shall display signal state per definitions in J2735:2016 along with the minimum and maximum time to signal transition.

* + 1. The Web UI shall provide a live status for coordination in the form of a pie graph representing the cycle length as the complete turn of the graph. Concentric circles represent the ring in which split times are represented. A position indicator represents the ideal position in the cycle and an alternate indication shall represent the actual position within the cycle and thus reflect the strategy for managing transition.

## Programming Displays

* + 1. Programming displays in the form of menus shall aid the operator in entering data from the front-panel keyboard.

2.2.1.1 A main menu shall allow the user to select a major function of the controller. A submenu shall then be displayed to allow the user to select a sub-function within the major function.

2.2.1.2 English language and traffic engineering terminology shall be used throughout to facilitate programming. The display organization shall allow traffic personnel to program the controller without using reference cards or manuals. All data entry and data screens shall be in logical order.

2.2.1.3 Four (4) arrow cursor key shall allow the user to scroll through all programmed data.

2.2.1.4 Programming entries shall consist of alpha-numerical values, YES/NO and ON/OFF entries. During program entry, the new data shall be displayed as it is entered. Warning and consistency checks shall be performed when ENTER or cursor key is pressed. On constrained data (entries that are constrained by other programmed data), entries shall only be stored when the consistency check is validated. On non-constrained data the entries shall be stored only when the new values are within valid value thresholds.

2.2.1.4.1 An example of constrained data is the sequence of the phases within a ring. They need to be checked with the phase compatibility, phases in the ring and start phases among others.

2.2.1.4.2 An example of non-constrained data is the vehicular extension time entry.

* + 1. The keyboard entry software shall include context sensitive help screens. Help information shall be accessed by placing the cursor on the data entry in question then pressing the HELP key. Help screens shall be provided for all keyboard-entered data and shall include at a minimum range, description, and functional operation information for the data entry.
1. Programing

## Programming Methods

* + 1. The methods listed below shall be available for controller configuration and timing entries. The manufacturer shall be able to provide as off-the-shelf items, all of the software, and software required to affect the listed methods, and to implement network operation with system masters and host PCs.

3.1.1.1 Manual data entry via the front panel keyboard.

3.1.1.2 Downloading from a portable PC-compatible computer via NTCIP interfaces.

3.1.1.3 Transfer from one controller to another, or restoring for a back-up copy, using a data key, USB thumb drive, or an SD card. (dependent upon controller hardware support)

3.1.1.4 A PC web interface shall allow the operator to view the current status and programming for the controller. It shall be possible to change programming from a remote device like a PC and if the device is connected to the controller, the programming changes shall take effect the next time that interval or function is used.

## Programming Security

* + 1. A minimum of three access levels shall be available to provide programming security.
			1. The highest or administrator level shall have access to all programming entries, including setting access codes and user security permissions.
			2. The second or data change level shall have read-write privileges for all programming entries except access codes and CRC enable/disable.
			3. The third or data display level shall only have access to read programmed data and status screen.
		2. User selectable, access codes shall be provided for the administrator and data change and read only access levels. Each user shall have a user name that is entered using from 6 to 15 characters. Access codes shall initially be set to provide unrestricted access.
		3. If there has been no keyboard activity the controller shall automatically logoff the user after 30 minutes.
		4. Unique user permissions shall allow for read/write access of each programming menu within a context of functionality provided within that menu.
		5. Unique user permissions shall allow for menu viewing access for each programming menu within a context of functionality provided within that menu.

## Programming Utility Functions

* + 1. A copy function shall permit copying all timing plan data from one phase to another. It shall also permit copying all timing plans from one timing plan to another, one detector plan and detector options plan to another, all event plan data from one event plan to another, one preemptor to another, and one sequence to another. This feature will facilitate data entry when programming any two or more phases with the same timing values, or detectors with the same programming, and/or two or more event plans with the same plan data.
		2. The controller unit shall contain a backup database with user specified values stored in non-volatile memory. A copy function shall permit transferring the backup database to the active database. The user shall be able to create their database and copy it to the default database.
		3. A sign-on message shall allow the user to view the controller software version number. It shall also be possible to display the sign-on message by keyboard selection. The sign-on display shall allow a user-defined message of up to two lines with 38 characters per line.
		4. The controller shall have the capability to output a memory image of the user programmed settings and intersection configuration data in binary format. This shall allow transferring the memory image data to a data key, USB thumb drive, or a SD card.
1. Actuated Control Functions

The controller software shall provide all actuated control functions and operations required by the NEMA TS2 Standard (subject to controller and cabinet I/O capabilities). In addition, it shall provide the features described in the following sub sections.

## Phase Sequence

* + 1. The phase sequence of the controller shall be programmable in any combination of sixteen phases, using up to 16 concurrent groups and four timing rings.
		2. Up to 16 unique phase sequences shall be user configurable and used in the controller.
		3. Phase sequence information shall be changeable from the keyboard and stored in data memory.
		4. The standard phase sequence of the controller shall also be capable of being altered by coordination, TOD or external alternate sequence command, by selection 1 of the 16 configurable sequences.
		5. An exclusive pedestrian phase feature shall be provided which will time and display the pedestrian indications with the vehicle movements remaining in all red.
		6. A ring group feature shall allow independent timing of rings 1&2 from rings 3&4.
		7. Phase concurrency shall be allowed to change across sequences. This shall be defined in the form of phase incompatibilities that extend past sequence barrier programming and the ability to add or remove phases unique to programmed sequence.
		8. Phase compatibility shall be unique per sequence and programmable within ring-barrier configuration menu.
		9. Sequence changes shall take effect immediately unless currently active or selected next phases will not also be compatible in the new sequence.
		10. Four independent backup prevention plans shall be provided. Each phase pair assignment shall have the ability to apply three strategies for restricting phase order selection:
			1. Inhibit absolutely for a permissive movement which does not cause cycle failure.
			2. Inhibit by placing locking artificial demand on an incompatible phase, often a side street, to safely sequence back to backup phase.
			3. Inhibit by placing non-locking artificial demand on an incompatible phase, often a side street, to safely sequence back to backup phase.
			4. Inhibit until all-red phase indications can be reached so yellow-trap situations can be avoided.

## Timing Intervals

* + 1. Timing intervals shall be programmable from 0-255 in one second increments or from 0-25.5 in one-tenth second increments, depending on the function.
		2. Four independent timing plans shall be provided. Any plan shall be selectable on a TOD basis, by event plan, or for one cycle following preemption.
		3. Each timing plan shall contain the following interval timings:

|  |  |
| --- | --- |
| Minimum Green | Maximum 3 |
| Bike Green | Dynamic Maximum |
| Delay Green | Dynamic Maximum Step |
| Conditional Service Minimum Green | Yellow Clearance |
| Walk  | Red Clearance |
| Walk 2 | Red Maximum |
| Walk Maximum | Red Revert |
| Pedestrian Clearance | Actuations before Reduction |
| Pedestrian Clearance 2 | Seconds per Actuation |
| Pedestrian Clearance Maximum | Maximum Initial |
| Pedestrian Carryover | Time before Reduction |
| Vehicle Extension | Cars Waiting |
| Vehicle Extension 2 | Time to Reduce |
| Maximum 1 | Min Gap |
| Maximum 2Advanced Pedestrian PhaseDelay PedestrianOverlap Trailing Yellow ClearanceDelay WalkPre-Clearance Green | Steps to ReduceAdvanced Pedestrian OverlapOverlap Trailing Green Overlap Trailing Red ClearanceAdvanced Walk |

* + - 1. The bike green interval shall replace the phase minimum green if the interval time is larger than the min green time and if a detector input designated as a bike detector has been activated.
			2. Two Walk and Pedestrian Clearance intervals shall be provided for each phase per timing plan. The second Walk and Pedestrian Clearance shall be activated by a time base event plan.
			3. Two vehicle extension intervals shall be provided for each phase per timing plan. The active vehicle extension interval shall be selected by a time base event plan.
			4. If enabled, a Delay Green timer shall delay the vehicle phase from starting until the timer has expired. This shall provide an additional all red for the vehicles movement and allow the pedestrian indications to precede the vehicle movement.
			5. The Pedestrian Walk interval shall extend from Walk to the smaller of the Walk Max time or the phase maximum in effect with a constant input from the “Walk Extension detector”. The Pedestrian Clearance interval shall also extend Pedestrian Clearance to the smaller of the Pedestrian Clearance Max time or the phase maximum in effect with a constant input from the “Walk Extension detector.”
			6. Volume density intervals shall include actuations before adding to the initial green and the number of cars waiting while the phase is not green. Actuations before added shall provide a user-specified number of actuations that must occur before adding variable (added) initial time. Cars waiting shall record the number of cars recorded while the phase is not green, the start of gap reduction is initiated by the time before reduction or the cars waiting which ever reaches its programmed value first.
			7. The controller shall be capable of dynamically extending the maximum green time for each phase based on vehicle demand. Three maximum green intervals shall be selectable per phase based on TOD event plan, or external input. The initial interval shall be selectable as Max 1, Max 2, or Max 3. If the phase terminates due to max-out for two successive cycles, then the maximum green time in effect shall automatically be extended by a dynamic max step interval on each successive cycle until it is equal to dynamic maximum. If the phase gaps out for two successive cycles, then the maximum green time shall be reduced by the dynamic max step time until it reaches to the original max value.
			8. Each phase shall have a red maximum timing interval. An input (red extension) shall extend the all red period of the assigned phase, as long as the detector input is true. This input must be true within the all red time of the assigned phase to be able to extend the all red period. If this detector fails, then the all red extension feature shall be disabled.
			9. Each phase shall have programmable delay walk interval to delay the onset of pedestrian walk after the beginning of green on parent vehicle phase.
			10. Each phase shall have programmable advanced walk interval to delay the start of parent phase vehicle green after the start of pedestrian walk.
			11. The controller shall provide a programmable fixed green clearance interval prior to the transition from green to yellow. This interval shall trigger advanced warning flasher or prepare to stop signalization.
		1. Guaranteed minimum interval values shall be settable and shall not be overridden by the controller. Values shall be provided for the following intervals:
			1. Minimum Green
			2. Walk
			3. Pedestrian Clearance
			4. Yellow Clearance
			5. Red Clearance
			6. Overlap Green

## Overlaps

* + 1. The controller shall provide 16 (sixteen) internally-generated overlaps (A - P). All overlap functions shall be programmable from the controller keyboard.
		2. The controller shall provide delay, leading pedestrian interval (LPI), trailing clearance times via four timing plans.
		3. The controller shall allow non-parent phases to begin timing during trailing clearance times via program entry for non-FYA type overlaps.
		4. The controller shall allow for program entry of overlap incompatibilities. The active overlap shall have hierarchy when conflicting incompatibilities exist.
		5. Overlaps shall be individually programmable as standard (normal), or flashing yellow arrow. Overlap green, yellow, red and lag interval timings shall be individually programmable with respect to the activation or termination of the parent phase, respectively.
			1. The standard overlap shall require only included phases to be configured.
			2. Standard overlaps shall support a per overlap setting to begin overlap green early when user definable phase(s) next selection has occurred.
			3. Standard overlaps shall support a per overlap setting to define the exiting and next phases that will cause green trailing intervals to be timed.
			4. A protected phase overlap shall operate according to the following rule:

a. Overlap shall be green, yellow, or red like a normal overlap except its outputs shall be blank when the protected phase is green, or the controller is transitioning to a non-included phase.

* + - 1. A pedestrian protected overlap shall be green under the following conditions:
1. When an included phase is green and the protected pedestrian is NOT in walk or pedestrian clearance.
2. When the controller is in transition between included phases and a pedestrian protected phase is not next.
3. After servicing an included phase pedestrian demand if there is enough time before max-out to service the overlap minimum green.
	* + 1. A phase protected overlap shall proceed to green when the programmed inhibited phase is not active per TS2 definitions and an included parent phase is active.
			2. A green phase protected overlap shall proceed to green when the programmed inhibited green phase is not in a green interval and an included parent phase is active.
			3. A green when next or advanced overlap programming shall allow the overlap to proceed to green when the selected included parent phase is next.
			4. The controller shall provide an overlap selection for Protected/Permissive Left Turn (PPLT) Flashing Yellow Arrow FYA operation. PPLT/FYA shall allow selection of the protected phase or protected overlap, permissive phase or permissive overlap, flash yellow output to either the overlap output, or a pedestrian clearance (yellow) output. The PPLT/FYA shall also allow the yellow arrow to continue flashing once the permissive through phase reaches yellow clearance if the selected next phase is the protected phase. PPLT/FYA shall be enabled on a TOD basis.
			5. The controller shall provide an overlap selection for Protected/Permissive Right Turn (PPRT) Flashing Yellow Arrow FYA operation. PPRT/FYA shall allow for selection of the protected phase or protected overlap, permissive through phase or permissive through overlap, and pedestrian phase protection. The PPRT/FYA shall also allow the yellow arrow to continue flashing once the permissive through phase reaches yellow clearance if the selected next phase is the protected phase.
		1. The controller shall provide the capability of sixteen pedestrian overlaps. These shall be capable of overlapping the pedestrian displays of any combination of phases with a pedestrian movement. Pedestrian overlaps shall support a locking and non-locking phase next decision.

## Conditional Service

* + 1. The controller shall provide a programmable conditional service feature. When selected, the controller shall service any leading phases once normal service to that phase has been completed and enough time for additional service exists on the concurrent even phase.
		2. A conditional service minimum green time shall be programmable for each phase. This interval shall ensure a minimum green if the phase is conditionally served.
		3. It shall be possible to program the controller to re-service secondary phases after conditionally serving the leading phases.
		4. The controller shall support a user definable double serve feature that allows two separate phases to be placed and timed in the sequence with separate phase/split timing, but output through one phase load-switch.

## Additional Features

* + 1. The following features shall be programmable for each phase in each of four separate timing plans:
			1. Locking/non-locking detector memory
			2. Vehicle Recall
			3. Pedestrian Recall
			4. Maximum Recall
			5. Soft Recall
			6. No-Rest Phase
			7. Enable Added Initial
		2. Also programmed by phase shall be:
			1. Phase in Use
			2. Exclusive Pedestrian Phase
		3. Soft recall shall return the controller to the programmed phase in the absence of other calls.
		4. If a phase is designated as a no-rest phase the controller shall not rest in the phase, and proceed to the next phase with serviceable demand, or if no demand is present the next soft recalled phase.
		5. The controller shall permit power start and external start to be programmed by phase and interval. Start intervals shall be green, yellow red, or yellow with overlaps forced yellow.
		6. During a power start condition, the controller shall be capable of timing an all-red or flash interval before the power start phase(s) and interval(s) are displayed. The controller shall support the MUTCD 2009 that requires a minimum of 6 seconds of all red following red/red flash.
		7. The controller shall provide guaranteed passage operation on a per phase basis. When selected, this feature shall provide a full passage (vehicle extension) interval when a phase gaps out with a gap in effect less than the vehicle extension interval (preset gap).
		8. The controller shall provide both single and dual entry operation. When selected, dual entry shall cause the controller to ensure that one phase is timing in each ring.
		9. It shall be possible via keyboard selection to inhibit the service of a phase with other phase(s) within the same concurrent group.
		10. The controller shall provide the following additional selectable pedestrian functions to all phases:
			1. Actuated phase rest in WALK
			2. Flashing WALK output
			3. Pedestrian clearance protection during manual control
			4. Pedestrian clearance through yellow
			5. Pedestrian timing shall be capable of being carried over from one phase to another
			6. HAWK (Pedestrian Hybrid Crossing Beacon) functionality that is not assigned to the sequence table.
			7. HAWK programming for ingress phase and travel that shall control the HAWK phase permissive windows under coordinated operation.
		11. Programming shall be provided to inhibit re-service of leading phases (left turns) within the same concurrent group. Also, programming shall be provided to place a demand on a phase in another concurrent group to cause the controller to leave the concurrent group prior to servicing the leading phases in the same concurrent group.
		12. The controller shall provide a programmable simultaneous gap termination feature. When programmed, phases in both rings shall gap out together in order to terminate the green interval and cross the barrier.
		13. The controller shall provide automatic flash selection per the requirements of the MUTCD. Both the flash entrance and exit phases shall be programmable through the keyboard, and flashing shall be controlled by either setting the fault/voltage monitor output to be FALSE or by flashing through the load switch driver outputs. If flash desired through the load switches, both the phase and overlap outputs shall be flashed either yellow or red as selected by the operator. Automatic flash shall be selectable by external input, system command, or TOD event plan.
		14. The controller shall provide the option to exit cabinet or power up flash through a unique sequence and timing plan through programmable settings. It shall also be possible to disable the NEMA specified phase cycle via artificial demand.
		15. The controller shall provide wig-wag and solid advanced warning flasher outputs which begin during the green pre-clearance period until the phase returns to green.
		16. The controller shall provide light rail phase assignment which allows the phase to gap-out independently of barrier crossings. Additionally, it shall support two and three lens signalization as defined in MUTCD 2009.

1. Coordination

Coordination functions to control intersection cycle lengths, system offset relationships, and phase split percentages shall be provided as a standard feature, with no need for additional modules or software.

## Event plans

* + 1. A minimum of 120 event plans shall be provided. Each plan shall allow selection of an independent cycle length, offset value, and split pattern. The event plans shall be selected using non-interconnected (time-base) coordination commands. Offset and Split values shall be entered in either seconds or percentages.
		2. The event plans shall be selected by the coordination command using the following formats:
		3. Pattern - This format shall allow selecting the event plans directly, that is, commanding Plan 1 selects Pattern 1. Pattern command shall include 1-120 patterns, pattern 254 shall select free, and pattern 255 shall select flash.
		4. TS2 - This format shall allow selecting the event plans as a function of Timing Plan and one of three offsets. With this format a minimum of 20 Timing Plans shall be available for selection of one of sixty event plans.
		5. The following functions shall be programmable in each event plan:
			1. Cycle length
			2. Split pattern number
			3. Offset value

5.1.5.3 Dwell/add time

5.1.5.4 Actuated coordination

5.1.5.5 Timing plan

5.1.5.6 Actuated walk rest

5.1.5.7 Phase sequence

5.1.5.8 Phase re-service

5.1.5.9 Maximum select

5.1.5.10 Fixed/Floating force off by phase

5.1.5.11 Split timing per phase in seconds or percentage

5.1.5.12 Coordinated phase split extension

5.1.5.13 Permissive timing

5.1.5.14 Coordinated phases

5.1.5.15 Coordinated reservice

5.1.5.16 Adaptive split operation by phase

5.1.5.17 Split value by phase

* + - 1. Omit by phase
			2. Min recall by phase
			3. Max recall by phase
			4. Pedestrian recall by phase

5.1.5.22 Special function outputs

## Cycle Length

* + 1. One cycle length shall be provided for each event plan. The cycle shall be adjustable over a range of 30-999 seconds in 1-second increments.
		2. The cycle length shall serve as the reference time for all coordination timing.

## Synchronization

* + 1. For systems with a system sync pulse, coordination timing shall be synchronized to the leading edge of that pulse, which shall serve as the master zero reference for all offset timing.
		2. After a valid system sync pulse has been received the coordinator shall check for the proper occurrence of the system sync pulse during each subsequent cycle. If a sync pulse does not occur, the coordinator shall self-sync and continue to operate with the last set of coordination commands for a programmable number of cycles from 0-255. If a sync pulse does not occur within the programmed period (or until the first sync pulse is received), the coordinator shall revert to the non-interconnected coordination mode.

## Offset

* + 1. Offset shall normally be defined as the time period from the system sync pulse to the beginning of the leading coordinated phase green (local zero). The coordinator shall also be capable of referencing the offset to the beginning of the lagging coordinated phase green, coordinated phase yield, start of yellow point or the start of the ring 1 coordinated phase
		2. Offsets shall be programmable using both percent and seconds. The range shall be from 0-99% of the cycle length in 1% increments or 0-255 seconds in 1-second increments.
		3. Offset changes shall be achieved by adding or subtracting cycle time over a maximum of three cycle periods to allow a smooth transition to the new offset. Other offset change methods shall be adding 33% or a user-selectable value to each cycle or to snap to the sync point once the permissive period is complete and the coordinated phases are green. Offset correction using dwell shall also be selectable.

## Split

* + 1. Each split shall provide a split interval for each of sixteen phases. The split interval shall be programmable using percent or seconds. The range shall be from 0-99% of the cycle length in 1% increments or 0-255 seconds in 1-second increments.
		2. Split interval settings shall determine the maximum time, including vehicle clearance (yellow and red), for a non-coordinated phase, or the minimum time for a coordinated phase. Phase termination shall be controlled by establishing a force-off point for each phase within the cycle. Except for the coordinated phases the force-off point shall be selectable to be a fixed point within the cycle or allowed to float. If floating force-offs are selected each phase shall time no more than its own split interval.
		3. During coordination, it shall be possible to operate a coordinated phase as actuated or non-actuated. If a coordinated phase is actuated, vehicle detections shall permit the coordinator to extend a phase beyond the normal yield point. Extended coordinated phase green shall be selectable using the same range as split interval settings (percent or seconds). If actuated coordinated phases are used, they shall be able to have actuated or non-actuated (walk rest) pedestrian movements.

## Permissive Periods

* + 1. Permissive periods shall be provided to control the time period during which coordinated phases are released to service calls on non-coordinated phases.
		2. All permissive timing shall begin at the lead coordinated phase yield point. A yield point shall be automatically computed for the coordinated phase in each ring. The coordinated phase yield points shall allow the coordinated phases to yield independent of each other. The yield point shall be the point at which the coordinated phase is released to allow the controller to service calls on non-coordinated phases. The computation shall take into account the coordinated phase split interval plus pedestrian and vehicle clearance times.
		3. Automatic permissive period operation shall be provided by automatically calculating a permissive period for each non-coordinated phase. The permissive period shall consist of a separate vehicle and pedestrian period computed from the phase split interval and the vehicle/pedestrian minimum time. The controller shall answer a call only during the associated phase permissive period. However, once the controller has been released to answer a call, all remaining phases shall be served in normal sequence.
		4. Single permissive period operation shall be provided by defining a single time period per cycle, beginning with the yield point during which the controller is allowed to answer phase calls for any phase. The duration of this period shall be selectable in each event plan.

## Phase Re-service

* + 1. If actuated coordinated phases are in use it shall be possible to re-service non-coordinated phases within the same cycle if sufficient time remains. A phase shall be re-serviced only if the permissive period for the phase indicates there is sufficient time remaining in the cycle to service the phase.
		2. Phase re-service shall be capable of being enabled/ disabled in each event plan.

## Transition Cycles

* + 1. It shall be possible to program the controller to ignore the pedestrian timing when calculating the minimum cycle length for offset correction.
		2. The controller shall provide a smooth and orderly transition when changing from free operation to coordinated operation and from one coordination command to another.
		3. During a free-to-coordinated transition, the controller shall not initiate a pick-up cycle and begin immediately upon receipt of a sync pulse or TOD time reference and a valid coordination command. The controller shall then enter coordination mode immediately and not await a phase or barrier termination.
		4. During any transition into coordinated control, the controller shall deduce from time spent in currently active phases the ideal position in the new effective cycle. The position may be unique to each ring or common to all rings within that ring-group based on a programmable setting.
		5. Each coordination command shall select a pattern. A command change shall be implemented concurrent with a sync pulse. Cycle, offset, and split changes shall not take effect until local zero.

## Crossing Arterial Control

* + 1. The coordinator shall be capable of implementing multiple coordinated phases per ring to support dual coordination at an intersection where two conflicting arterials require green platoon progression priority.

## Adaptive Split Demand

* + 1. The coordinator shall provide a method to select phases that allowed for adaptive split adjustment.
		2. The adaptive split algorithm shall reference a phase’s green utilization for adjustment using the Green Occupancy Ratio, as defined by the accumulated occupancy of vehicles over the stopbar detector(s) in relation to the total green duration.
		3. The adaptive split algorithm shall recompute the balance of split five seconds after each phase termination and make adjustments within the cycle.
		4. The adaptive split algorithm shall determine which phase or phases had excess time that was not used during the last measurement period. Then the excess time shall be rebalanced among phases configured to adapt splits with a green ration above 80 percent.

## Free Mode

* + 1. The coordinator shall provide a free mode of operation, where all coordination control is removed.
		2. Free mode operation shall be selectable by coordination commands, by external input or by keyboard entry.
		3. The coordinator shall revert to the free mode when active controller inputs or functions would interfere with coordination. Such inputs or functions shall include the following:
			1. Manual control enable
			2. Stop time
			3. Automatic flash
			4. Preemption
		4. The coordinator shall provide an active free mode, where coordination control is removed but the coordinator continues to monitor system sync to keep its timing in step with the system master or time of day if operating under TOD or central system control.

## Manual Control

* + 1. The controller shall allow manual override of the current coordination command from the keyboard. The manual command shall allow selection of any event plan to be in effect.

## Interconnect Modes

* + 1. The coordinator shall be compatible with fixed-time interconnect, which provides the sync pulse superimposed on the offset lines.
1. Preemption

The controller shall provide a minimum of 16 preemption sequences that can be programmed as either railroad-fire-emergency or bus vehicle preemption sequences. Preemption capability shall be standard and shall not require additional modules or software.

## Railroad-Fire-Emergency Vehicle Preemption

* + 1. The 16 railroad-fire-emergency vehicle preemptors shall be selectable as a priority or non-priority type. Priority preemptor calls shall override non-priority preemptor calls. Low-numbered priority preemptors shall override higher-numbered priority preemptor calls. Non-priority preemptor calls shall be serviced in the order received.
		2. Each preemptor shall provide a locking and non-locking memory feature for preemptor calls. If a preemptor is in the non-locking mode and a call is received and dropped during the delay time, the preemptor shall not be serviced.
		3. Preemptor timing intervals shall be programmable from 0-255 in one-second increments or 0-25.5 in one-tenth second increments, depending on function. Delay, max presence, and duration timing intervals shall be programmed from 0 – 65535 seconds in one-second increments.
		4. A programmable delay time interval shall be provided to inhibit the start of the preemption sequence. This interval shall begin timing upon receipt of a preemption call. This time shall be programmable from 0-65535 seconds in one-second increments.
		5. An inhibit time shall be provided as the last portion of the delay time interval. During this time, phases that are not part of the preempt sequence shall be inhibited from service. This time shall be programmable from 0-255 seconds in one-second increments.
		6. A programming option shall be available that allows termination of all phases prior to entering preemption.
		7. A programmable extend input shall cause the preemptor to remain in the dwell interval following the removal of the preempt call. If a preempt call is reapplied during this time, the preemptor shall revert to start of dwell interval. This time shall be programmable from 0-25.5 seconds in one-tenth second increments.
		8. A programmable duration time shall be provided to control the minimum time that a preemptor remains active. This time shall be programmable from 0-65535 seconds in one-second increments.
		9. A programmable maximum presence time shall be provided to control the maximum time that a preemptor input remains active and still be recognized by the controller. Once failed, the input must return to inactive state to be recognized again. This time shall be programmable from 0-65535 seconds in one-second increments.
		10. Phases timing at the beginning of a preemption sequence shall remain in effect for a minimum time before the controller advances to the next sequential interval. If the phase has been timing for longer than the programmed preemptor minimum time, the controller shall immediately advance to the next sequential interval. Minimum times shall be programmable for the following intervals:
			- 1. Green/walk/pedestrian clearance
				2. Yellow
				3. Red
		11. A phase shall advance immediately to pedestrian clearance if it has been timing a WALK interval at the beginning of a preemption sequence. It shall be possible to time the minimum pedestrian clearance through the yellow interval, or alternately to advance immediately to yellow. During preemption, pedestrian indicators shall be selectable as being a solid DONT WALK, OFF (blank), or fully operational.
		12. If an overlap is in effect when the preemption sequence begins, it shall be possible to terminate the overlap so that it remains red for the remainder of the preemption sequence. Overlaps terminating or forced to terminate shall time the preemptor minimum yellow and red clearance times.
		13. Each preemptor shall provide user-programmable green, yellow, and red track clearance intervals. These shall begin timing immediately after the preemptor minimum red interval.
		14. Each preemptor shall provide a user-programmable gate down extension and gate down max green. The gate down extension shall extend the track clearance green time after the gate down input in received. The gate down max green is the maximum the track clearance green time will be extended. If the gate down max green is exceeded the intersection shall be forced to flash.
			1. The forced flash shall be programmable to be either hard or soft. If hard flash is enabled, then exiting flash to normal operation shall require either pressing the clear key or the MMU reset. A programmed soft flash shall automatically return to normal once the fault condition no longer exits. For gate down fault this shall be once preemption is deactivated.
		15. Up to four permissive phases shall be selectable as track clearance phases. During the track clearance period, the selected phases shall time the track clearance green, yellow, and red intervals once, and then advance to the hold interval. If track clearance phases are not selected the track clearance interval shall be omitted from the preempt sequence. Controller interval timing shall be used if track clearance interval times have been programmed as zero.
		16. The preemption hold interval shall begin immediately after track clearance. It shall remain in effect until the preemptor duration time and minimum hold times have elapsed and the preemptor call has been removed or the preemptor maximum time has been exceeded. During the preemption hold interval, any one of the following conditions shall be selectable:
			- 1. Hold phase green
				2. Limited phase service
				3. All red
				4. Flash
		17. Any valid phase, except a track clearance phase, shall be selectable as a hold phase. If hold phases are not selected, the controller shall remain in all red during the hold interval. If flash is selected for the hold interval, up to two permissive phases shall be selectable to flash yellow, and the remaining phases shall flash red. Overlaps associated with the phases flashing yellow shall also flash yellow unless they have been forced to terminate, in which case they shall remain red.
		18. The preemptor shall immediately cause flashing operation if the preemption input and the track interlock input are not in opposite states and the track interlock function is enabled.
		19. Each preemptor shall provide a user-programmable green, yellow, and red dwell interval, during which the dwell phase(s) shall operate normally, except that the minimum green interval time shall equal the hold green time. At the completion of the dwell green interval, the controller shall time the dwell yellow and red clearance intervals prior to transfer to the exit phases.
		20. Up to four permissive exit phases shall be selectable to time after the preemption sequence has been completed. These shall serve as transition phases to return the controller to normal operation. It shall also be possible to place calls on selected phases upon exiting preemption. The option shall be provided to cause the preemptor to exit preemption to the correct phase to maintain coordination.
		21. Each preemptor shall provide a user-selectable exit timing plan. Upon exiting the preemption sequence, this timing plan shall serve as the phase times in effect for one controller cycle for all phases.
		22. Preemptor linking shall permit preemption sequences, where lower-priority preemptors may call the higher-priority preemptors from their preemption sequence.
		23. Each preemptor shall allow programming of an inhibit extension timer. This timer shall be the length of time the max call.
		24. Preemptor active outputs shall be provided for each of the preemptors. The output shall be set to ON when the preemption sequence begins and shall remain ON for the duration of the sequence. It shall also be possible to program preempt active outputs to be ON only during preempt hold intervals. Additionally, it shall be possible to program the non-active, non-priority preemptor outputs to flash while another preemptor is active.
		25. Preemptors shall normally override automatic flash. It shall be possible to inhibit this feature for each preemptor.
		26. Each preemptor shall, if free one cycle is enabled, allow the preemptor to exit to free for one cycle before returning to coordination.
		27. Each preemptor shall, if exit phase once is enabled, allow the preemptor to exit to specific phases in free mode (for a special minimum green time as defined in a new timing plan) before returning to coordination.

## Preemption Safeguards

* + 1. If a preemptor call is active when power is restored to a controller, the fault/voltage monitor output shall be set to FALSE, placing the intersection in flash. Similarly, if external start is applied during a preemption sequence, the intersection shall be set to flash. Intersection flash shall remain in effect until the preemptor call has been removed and the preemptor duration time has elapsed.
		2. An input shall be provided to stop timing of the current active preemptor under control of the MMU/CMU.
		3. A preemptor safety interlock shall be provided to cause the intersection to go into flash whenever the controller has been replaced and/or has not been programmed for preemption. This shall be achieved with an appropriate signal to the MMU/CMU.
		4. A preemptor deadlock protection timer shall monitor for a stuck state transition exceeding 90 seconds. Upon expiration of the timer, a latched critical flash shall apply.

## Signal Control and Priority

* + 1. A SCP preemptor shall be programmed to accept either a 6.25 pulse-per-second signal with a 50% duty cycle or a solid input to identify a SCP preemptor call. SCP calls shall be capable of preemptor call memory and shall be served based upon class level and ETA.
		2. SCP timing intervals shall be programmable from 0-255 in one-second increments or 0-25.5 in one-tenth second increments depending on the function.
		3. A re-service time shall be provided to avoid excessive utilization of the same SCP preemptor. If a call is received before the re-service time has elapsed, the SCP preemptor shall not be re-serviced. If re-service time has not been entered, then all phases with a call when leaving the SCP preemption sequence shall be serviced before the SCP preemptor may be served again.
		4. SCP preemptors shall provide delay, inhibit, and maximum time functions similar to those for railroad-fire-emergency vehicle preemptors described above.
		5. It shall be possible to program the controller to allow concurrent phases to be serviced for a SCP preemptor with only one phase selected as the hold interval phase.
		6. The controller shall include a transit signal priority algorithm that provides for transit vehicle movement through the intersection, while not interrupting coordination or skipping phases.
		7. A check-in detector input shall be provided that senses the arrival of the transit vehicle. When active this input shall initiate TSP.
		8. A TSP delay shall delay the beginning of TSP operation until a set interval after check-in.
		9. A check-out detector input shall determine the departure of the transit vehicle.
		10. Assignment of a single pulse from the check-in detector and check-out detector to the controller inputs shall be programmable to any controller input.
		11. When under coordination, the TSP sequence shall use adjusted split times to accommodate transit vehicles while maintaining coordinated control where permitted by class type of priority vehicle.
		12. When under free operation, the TSP sequence shall use adjusted maximum times to accommodate transit vehicle while not skipping phase.
		13. The controller shall support NTCIP 1211 priority request server messages that include service request with class type, class level, sixteen-character custom vehicle identifier, estimated time of arrival, and estimated time of departure information.

1. Time-Based Control (Non-Interconnected) Coordination

The controller shall include time-based control. This capability shall be a standard feature and shall not require additional modules or software.

## Clock/Calendar Functions

* + 1. The controller shall provide a TOD clock, which shall be used for all time-based control functions. The only required clock settings shall be the current time (hour, minute, and second) and date (month, day, and year). Day of week and week of year shall be automatically computed from the date setting. It shall also be possible to set the number of hours that the local standard time is ahead or behind Greenwich Mean Time
		2. During normal operation, the TOD clock shall use the power line frequency as its time base.
		3. In addition to entering time and date via the keyboard, it shall be possible to download the information from a central computer or a system master.
		4. The controller shall include a time reset input. This feature shall reset the TOD clock to 03:30 whenever the time reset input is TRUE.
		5. The controller shall provide support for a serial input from a GPS clock that supports the NMEA protocol to set the time and date of the controller.
		6. The TOD clock shall automatically compensate for leap year and shall be programmable to automatically switch to daylight savings time.

## Time-Based Control

* + 1. Time-based control shall utilize an event plan program format. The month program shall consist of 200 programmable schedules, each assignable to one of 16 (sixteen) day programs. Each day program shall consist of from 1 to 50 program steps, which define a program for the entire day. Each program step shall be programmed with a starting time and an event plan number. The event plans shall also be assigned to days of the week and days of the month.
		2. Time based control shall use event plans to assign:
			1. Vehicle detector plan number
			2. Controller sequence
			3. Timing plan
			4. Vehicle detector diagnostic plan
			5. Pedestrian detector diagnostic plan
		3. Time based control shall also use event plans to enable:
			1. Automatic flash
			2. System override
			3. Detector log
			4. Special functions
			5. PPLT/FYA
			6. Conditional delay
			7. Auxiliary functions
			8. Logic Processor statements
			9. By-Phase functions
			10. Pedestrian recall
			11. Walk 2 enable
			12. Vehicle extension 2 enable
			13. Vehicle recall
			14. Vehicle max recall
			15. Max 2 enable
			16. Max 3 enable
			17. Conditional service inhibit
			18. Phase omit
		4. There shall be a minimum of 36 holiday or exception day programs, which override the normal day program. Holiday programs shall be capable of being set as floating (occurs on a specific day and week of the month) or fixed (occurs on a specific day of the year). It shall be possible to program a fixed holiday so that it automatically repeats in the following year.
		5. It shall be possible to manually force any of the event plans to override the current event plan. The forced plan shall be entered from the keyboard and shall remain in effect until removed.

## Time-Based Coordination

* + 1. A minimum of 120 time-based schedule programs shall be available for the day-programs. These shall not have to be entered in any special sequence. It shall be possible to add and delete steps from a day-program without affecting any other day-program. Each of the program steps shall permit selection of the following functions:
			1. Day program assignment
			2. Start time
			3. Event plan
		2. Selection of system override in an event plan shall allow the event plan selected by the event plan to override the current telemetry or hardwire system commanded event plan.
		3. When operating in the time-based coordination mode the synchronization point for all cycles shall be referenced to a user selected reference time (sync reference), last event, or last sync as selected from the keyboard. The sync reference time is that time at which all cycles shall be reset to zero.
		4. If the sync reference time is selected, the synchronization point for the cycle selected by the current program step shall be computed using the present time, sync reference time, and cycle length. The synchronization point shall occur whenever the present time is such that an even number of cycle length periods has occurred since the sync reference time.

1. Cabinet

## Cabinet Functions

* + 1. The controller shall provide support for the following cabinet types with health monitoring of SDLC communications to all respective devices:
			1. Caltrans 33x
			2. NEMA TS1
			3. NEMA TS2 Type 1 and Type 2
			4. ITS Cabinet
			5. ATC Cabinet
		2. The controller shall provide pre-allocated default mapping unique to each cabinet type device. This mapping shall be reconfigurable through user programmable screen remapping. Each device pin default shall be restorable via user interface selection.
		3. The controller shall provide a real-time status for all configurable input/output pins alongside a brief description of the currently mapped function for that pin.
		4. The controller shall provide a summary of the devices that triggered a Response Frame Error (RFE) critical failure over the past 24 hours.
1. Detectors

## Detector Functions

* + 1. The controller shall provide a minimum of 64 vehicle detector inputs. Each input shall be assignable to one or more phases and be configured with optional detector functions.
			1. Extend and delay timing shall be provided for each detector.
			2. Delay timing shall be programmable to take affect only during green, only when not timing, and always.
			3. Each detector shall be capable of operating in a lock or non-lock mode.
		2. The controller shall also be capable of providing 16 pedestrian detector inputs. Each pedestrian detector shall be assignable to one or more phases.

## Detector Cross Switching

* + 1. The controller shall provide detector cross switching, permitting a vehicle detector to alternately place calls on assigned phases and assigned cross switch phases.
			1. If the assigned phase is not green and the cross-switch phase is green, the detector shall place calls on the cross-switch phase.
			2. If the assigned phase is omitted for any reason, the detector shall place calls on the cross-switch phase.

## Detector Types

* + 1. Each vehicle detector shall be user-programmable to operate with the following features:
			1. Extend: This detector will disconnect from extending the phase during green if either there is a gap in the detector input OR the detectors disconnect time times out. The detector’s input must be active before the phase turns green for this detector to operate.
			2. If the detector input is active when the phase turns green the detector passage time is reset, as long as the input is active. When the detector input becomes inactive the detector passage timer begins running. If addition calls are received before the detector passage timer expires, the detector passage timer shall be reset until the input becomes inactive. Once the detector passage timer times out the detector is disconnected for the balance of the phase green.
			3. Call: The detector shall place calls on the phase only when the phase is NOT green.
			4. Red extend: Actuation on a red extend detector during the red clearance of the assigned phase shall extend the red up to the red max phase time.
			5. Bike detector: Activation shall cause the phase to use the Bike minimum green in place of the minimum green.

## System Detectors

* + 1. Vehicle detectors shall be capable of being assigned to a minimum of 16 speed detectors. Speed shall be detected using both one and two detector configurations. Speed shall be computed using a keyboard entered average vehicle length and loop length for a one-detector configuration. When using two detectors, speed shall be calculated using a keyboard-entered distance between detectors and travel time between detectors.
1. Logic Processor Commands

## Standard Logic Commands

* + 1. One Hundred (100) logic processor commands shall be assessable from the front panel of the controller or through remote database management software.
		2. Each logic command shall be allowed to be enabled or disabled by TOD control.
		3. Each logic command shall consist of 10 “IF” statements, 5 “THEN” statements and 5 “ELSE” statements. At least 1 “IF” statement and 1 “THEN” statement is required to create a valid command.
			1. “IF” statements shall be logically grouped using Boolean gating including:
	+ AND
	+ OR
	+ XOR
	+ NAND
	+ NOR

		- 1. When combined “IF” statements create one single “TRUE” result, the “THEN” statements shall be implemented.
			2. When combined “IF” statements create one single “FALSE” result, the “ELSE” statements shall be implemented.
			3. The “IF” statements shall monitor controller functions and timer including, but not limited to the following:

|  |  |
| --- | --- |
| Phase/Overlap Green | Phase Calls |
| Phase/Overlap Yellow | Phase Omits |
| Phase/Overlap Red | Phase Force Offs |
| Phase/Overlap Active | Detector Fails |
| Detector Plan Number | Ring Timers  |
| Detector Activity | Preemption Activity |
| Detector Volume | Preemption Dwell |
| Detector Occupancy | Preemption Exit |
| Coordination Timers | Stop Time |
| Coordination Holds | Manual Control |
| Phase Holds | Overlap timers (lagging/leading grn) |
| Phase Walks | Active Event Plan Number |
| Phase Don’t Walks | TBC parameter |
| Phase Next | Input / Output Bits |
| Phase ChecksSCP Active | Logic FlagsSCP ETA |

* + - 1. The “THEN” and “ELSE” statements shall be used to control functions and events (on/off) in the controller, including, but not limited to, the following:

|  |  |
| --- | --- |
| Set Logic Flags  | Set Phase/OVL Greens  |
| Set Phase calls | Set Phase/OVL Yellows  |
| Set Input or Output Bits | Set Phase/OVL Reds  |
| Set Delays | Set Walk / Ped Clear / Don’t Walk |
| Set Load Switch circuits (R/Y/G)  | Set Pedestrian Detectors |
| Set Vehicle Detectors | Set Holds |
| Omit Phase / Peds  | Set Force-offs |
| Change Max Timers (max1, max 2) | Set Red Rest |
| Set Stop Time | Set Flash  |
| Set Alarms | Set External Start |
| Set Recalls, CNA1, CNA2, etc. | Set Manual Control Enable |
| Set Coordination Plan | Set Manual Advance |
| Set Free | Call Preempt |
| Call TSP | Call Phase |
| Set Event plan | Set Timing Plan |

## Extended Options Logic commands

* + 1. An additional 100 logic commands (101-200) shall be available in the controller, but hidden from front panel access.
			1. These commands shall be stored and used for easy implementation of common logic required at multiple intersections.
			2. These commands shall only be assessable through computer software and shall be uploaded and downloaded to the controller using the same computer software.
		2. Once downloaded to the controller, the logic shall be activated using an extended options file.
			1. This file shall be downloaded to the controller and used to initialize the logic statements.
			2. This file shall be used to create a custom menu on the controller. From this menu the user shall be able to enable or disable any of the extended options hidden in the upper one hundred logic commands.

1. System Communications

## System Commands

* + 1. All mode and special function commands shall be cleared after 20 minutes of loss of communication between controller and system master or central system.
		2. The status of each of the following functions shall be transmitted to the central system in response to a local status request:
			1. Green and yellow status for all phases and overlaps
			2. Walk and pedestrian clearance status for all phases
			3. Vehicle and pedestrian detector status
			4. Phase termination status
			5. Local time
			6. Coordination status
			7. Command source
			8. Sync or transitioning status of coordinator
			9. Conflict flash status
			10. Local flash status
			11. Preempt activity and calls
			12. Volume and occupancy data from a minimum of 16 system detectors
			13. Speed data from a minimum of two speed detectors
			14. Maintenance required (cabinet door open) status

11.1.2.14.1 Status of two user-defined alarms

* + 1. The status of each of the following parameters shall be calculated on a per-cycle basis and transmitted to the or central system for Split Monitor Reporting:
			1. Actual time spent in each phase
			2. Time of day at end of cycle
			3. Phases forced off during cycle
			4. Type of coordination operation
			5. Whether transitioning to new offset
			6. Cycle, offset, and split in effect during last cycle
			7. Flash status if operation is Free
		2. Upload/Download Capability – The controller shall provide the capability to upload/download the entire intersection database. Phase assignments for overlaps and preemptors shall not be downloaded to preclude unsafe controller operation. Data transfer shall not require the intersection to be in flash.

## System Communications

* + 1. In the event of a communications failure, the controller shall revert to the non-interconnected coordination mode after it has self-synchronized for a number of cycles, which shall be selectable from 1-254. If the number of cycles is set to 255, the controller will self-synchronize until a synchronizations pulse is detected.

## Communications Protocols

* + 1. The controller shall have the capability of supporting communications with traffic management systems using industry standard protocols with the installation of appropriate optional software.
		2. At a minimum the controller shall have optional software to support the following protocols:
			1. Caltrans AB3418
			2. NTCIP Level 2 as defined by Section 3.3.6 of NEMA TS2- 2003. NTCIP v02.06 capabilities shall include all NTCIP mandatory and optional objects. The controller vendor shall provide access to all controller data via vendor specific objects. These and all other objects supported by the controller shall be defined in a standard MIB file.

## Ethernet Communications

* + 1. The controller shall have the capability of supporting Ethernet communications, using TCP/IP communications protocols.
		2. This communications protocol shall utilize the controller’s built-in switches and shall not require Ethernet-to-Serial converters.

## External Clock

* + 1. The controller shall have the capability of communicating with an external clock like a GPS clock for setting its internal time of day clock.
		2. The controller shall include a time reset input. This feature shall reset the TOD clock to 03:30 whenever the time reset input from the GPS of WWV clock is TRUE.
1. Diagnostics

## General Diagnostics Features

* + 1. The controller shall include both automatic and operator-initiated diagnostics. This capability shall be a standard feature and shall not require additional modules or software.
		2. Automatic diagnostics shall verify memory and microprocessor operation each time power is reapplied to the controller. After power has been applied, diagnostics shall continually verify the operation of essential elements of the controller including communications.

## Detector Diagnostics

* + 1. TOD controlled detectors diagnostics shall be provided that allow testing vehicle and pedestrian detectors for no activity, maximum presence, and erratic output.
		2. A minimum of four detector diagnostic plans shall be provided. These plans shall be selectable on a TOD basis. This shall allow varying the detector diagnostic intervals to correspond with changes in detector activity.
		3. If a detector is diagnosed as failed, the associated phase shall be placed in one of the following keyboard selectable modes:
			1. Detector fail recall from 1 to 255 seconds.
			2. Disable the detector from calling or extending.
		4. Diagnostics for NEMA TS2 detectors connected to the controller using a Bus Interface Unit (BIU) shall also include detection of watchdog, open and shorted loop, and excessive inductance change failures.
1. Logging

The controller shall be capable of logging and reporting detector activity, detector failures, and the occurrence of selected events or alarms. Logs shall be capable of being printed or displayed on the front of the controller.

## Detector Logging

* + 1. The controller shall include a detector log buffer capable of logging volume, occupancy and average speed for selected vehicle and speed detectors.
		2. The detector-logging interval shall be keyboard selectable as 5, 15, 30, or 60 minutes.
		3. Detector logging shall be capable of being enabled or disabled by TOD.

## Detector Failure Logging

* + 1. The controller shall include a detector failure log buffer capable of storing a minimum of 100 time and date-stamped detector failure events. Once logged, detector failure events shall remain in the log until cleared or the log buffer capacity is exceeded at which time the oldest detector failure events shall be overwritten.
		2. All detector diagnostic failures shall be recorded in the detector failure log including: no activity, maximum presence, erratic output, watchdog failure, open loop, shorted loop, and excessive inductance change. If a detector recovers after a diagnostic failure, a detector on-line event shall be stored in the detector failure log.
		3. Detector failure logging shall be capable of being disabled.

## Event Logging

* + 1. The controller shall include an event log buffer capable of storing a minimum of 200 time and date-stamped events or alarms. Once logged, events shall remain in the buffer until cleared or the log buffer capacity is exceeded at which time the oldest events shall be overwritten.
		2. At a minimum, the following events shall be logged: communication failures, coordination faults, MMU and local flash status, preempt, power ON/OFF, low battery, and status of a minimum of two alarm inputs. An on-line event shall be logged when an event or alarm returns to normal status.
		3. If security is enabled, an event shall be logged when a user enters a data change. This event shall include the user’s ID. It is necessary to log the first change only and not every change. Also, an entry shall be recorded when a user logs in and out of the controller.
		4. Event logging shall be capable of being enabled or disabled for each category of event or alarm.